

In the Claims

Cancel claims 1-24 and add new claims 25-47 as follows.

-- 25. A data storing device comprising:

a housing including first and second opposed portions;

an integrated circuit coupled to the first portion of the housing, the integrated circuit including a random access memory;

a battery supported by the first portion of the housing and having first and second terminals, the first terminal being coupled to the integrated circuit; and

connection circuitry coupling the second terminal of the battery to the integrated circuit to complete a circuit, the connection circuitry including a conductor supported by the second portion of the housing and movable with the second portion of the housing.

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26. A data storing device in accordance with claim 25 wherein the battery is a thin film battery.

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27.

A data storing device according to claim 25, wherein the conductor completes a circuit and supplies electrical power to the memory when the first and second portions of the housing are sealed together and does not complete the circuit or supply electrical power to the memory when the first and second portions are not sealed together.

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28.

A data storing device according to claim 25, wherein the conductor completes a circuit and supplies electrical power to the memory when the first and second portions of the housing are coupled together and does not complete the circuit or supply electrical power to the memory when the first and second portions are not coupled together.

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A data storing device according to claim 25, wherein the first and second portions of the housing hermetically seal the integrated circuit and the battery.

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A data storing device according to claim 25, wherein the first and second portions of the housing hermetically seal the integrated circuit and the battery when the first and second portions of the housing are mated together, and wherein the conductor completes a circuit and supplies electrical power to the memory when the first and second portions of the housing are mated together and does not complete the circuit or supply electrical power to the memory when the first and second portions are not mated together.

<sup>1</sup>  
~~31~~. A data storing device comprising:

a housing defined by first and second housing portions, the second housing portion being movable relative to the first housing portion between mated and open positions;

an integrated circuit supported by the first housing portion;

a battery in the housing; and

a conductor supported by and movable with the second housing portion, the conductor coupling the battery to the integrated circuit when the second housing portion is in the mated position.

<sup>8</sup>  
~~32~~. A data storing device in accordance with claim <sup>1</sup>~~31~~ wherein the integrated circuit comprises a static random access memory.

<sup>9</sup>  
~~33~~. A data storing device in accordance with claim <sup>1</sup>~~31~~ wherein the integrated circuit includes a memory and a microprocessor, and wherein the conductor couples the battery to the integrated circuit.

<sup>10</sup>  
~~34~~. A data storing device in accordance with claim <sup>1</sup>~~31~~ wherein the integrated circuit includes a memory and a microprocessor, wherein the memory is a static random access memory, and wherein the conductor couples the battery to the integrated circuit so that the integrated circuit is powered by the battery, thereby resulting in the static random access memory being powered by the battery.

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~~36~~

A data storing device in accordance with claim ~~31~~<sup>7</sup> wherein the battery comprises a thin film battery.

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A data storing device in accordance with claim ~~31~~<sup>7</sup> wherein the housing has a thickness of about 0.03 inches.

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~~37~~

A data storing device in accordance with claim ~~31~~<sup>7</sup> wherein the integrated circuit includes a memory, an RF transmitter, and a microprocessor, wherein the memory is a static random access memory, and wherein the conductor couples the battery to the integrated circuit so that the integrated circuit is powered by the battery, thereby resulting in the static random access memory being powered by the battery.

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A data storing device in accordance with claim ~~31~~<sup>7</sup> wherein the integrated circuit includes a memory, a microwave transmitter, a microwave receiver, and a microprocessor, wherein the memory is a static random access memory, and wherein the conductor couples the battery to the integrated circuit so that the integrated circuit is powered by the battery, thereby resulting in the static random access memory being powered by the battery.

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A data storing device in accordance with claim ~~31~~<sup>7</sup> and further comprising conductive epoxy coupling the battery to the integrated circuit.

<sup>14</sup>  
~~40~~. A data storing device according to claim <sup>7</sup>~~31~~, wherein the first and second housing portions enclose and hermetically seal the integrated circuit and the battery when the first and second housing portions are in the mated position.

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<sup>17</sup>  
~~41~~. A data storing device according to claim <sup>7</sup>~~31~~, wherein the conductor does not supply electrical power to the integrated circuit when the first and second housing portions are not in the mated position.

<sup>18</sup>  
~~42~~. A data storing device according to claim <sup>7</sup>~~31~~, wherein the conductor completes a circuit and supplies electrical power to the integrated circuit when the first and second portions of the housing are sealed together and does not complete the circuit or supply electrical power to the integrated circuit when the first and second portions are not sealed together.

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43.

A portable data storing device comprising:

a housing defined by first and second housing portions each including planar surfaces;

an integrated circuit including a static random access memory configured to store the data, the integrated circuit being supported from the first housing portion;

a thin film battery in the housing; and

a conductor supported by and movable with the second housing portion, the conductor coupling the battery to the integrated circuit so that the integrated circuit is powered by the battery when the first and second portions are mated and thereby resulting in the static random access memory being powered by the battery and so that the integrated circuit is not powered by the battery when the first and second portions are not mated.

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44.

The portable data storing device of claim 43, wherein the integrated circuit further comprises a microprocessor, a spread spectrum RF transmitter controlled by the microprocessor, an RF receiver controlled by the microprocessor.

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A portable data storing device in accordance with claim 43 wherein the housing has a thickness of about 0.03 inches

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A portable data storing device in accordance with claim 43 and further comprising conductive epoxy electrically coupling the battery to the integrated circuit.

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A portable data storage device comprising:

a first housing member;

an antenna formed on the first housing member;

a second housing member configured to be mated to the first housing member;

a first battery disposed between the first and second housing members, a first electrode of the first battery contacting a first power conductor on the first housing member;

02 a second battery disposed between the first and second housing members, a first electrode of the second battery contacting a second power conductor on the first housing member;

an integrated circuit disposed on a side of the first housing member configured to be mated to the second housing member; and

a conductor formed on the second housing member, the conductor coupling the first and second batteries in series and supplying electrical power to the integrated circuit when the second housing member is mated to the first housing member and not coupling the first and second batteries in series or supplying electrical power to the integrated circuit when the second housing member is not mated to the first housing member.

48. The portable data storage device of claim 47, wherein the rated circuit further comprises a microprocessor, a RF transmitter controlled by the microprocessor, an RF receiver controlled by the microprocessor and a static random access memory coupled to the microprocessor and configured to store the data, the RF transmitter and RF receiver being operatively coupled to the antenna.--

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